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(54) Nonvolatile semiconductor memory device having test circuit for testing erasing function thereof

(57) The nonvolatile semiconductor memory of the present invention has erase circuits 30 for supplying predetermined voltage to corresponding blocks, respectively. Each of the erase circuits 30 comprises an erase

address detection circuit 33 for detecting whether an erase transistor 31 conducts a switching operation in accordance with a block address signal.

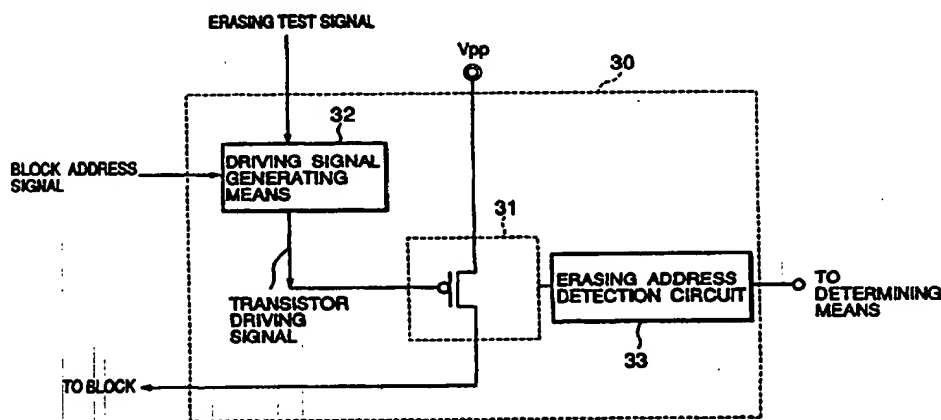


Fig. 3

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EUROPEAN SEARCH REPORT

Application Number
EP 97 12 0024

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
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			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			G11C
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 16 Apr11 1999	Examiner Sarasua Garcia, L
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ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

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